

03/17/99
JC533 U.S. PTO

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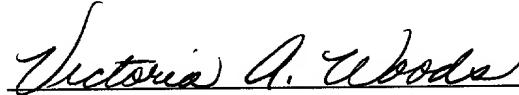
In re Application of:) PATENT APPLICATION
Inventor: David Russell Evans)
Serial No.: Not Yet Assigned)
Attorney Docket No.
Filed: Herewith) SMT 335
Title: METHOD FOR MODIFICATION)
OF POLISHING PATTERN DE-)
PENDENCE IN CHEMICAL)
MECHANICAL POLISHING)

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03/17/99


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Victoria A. Woods
Signature Date: March 17, 1999

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Transmitted herewith for filing is a patent application identified as follows:

Inventor: David Russell Evans

Title: METHOD FOR MODIFICATION OF POLISHING PATTERN
DEPENDENCE IN CHEMICAL MECHANICAL POLISHING

No. of pages in Specification: 10 No. of Claims: 9

No. of sheets of Drawings: 18 Formal: , Informal: X

Also enclosed are:

- A Declaration.
- An Assignment and Recordation Form Cover Sheet.
- Power of Attorney by Assignee.
- A certified copy of a priority application.
- A Verified Statement to establish small entity status.
- An Information Disclosure Statement under 37 C.F.R. §1.56.

The filing fee pursuant to 37 C.F.R. §1.16 is determined as follows:

No. Filed	No. Extra	Rate Small Entity/ Other Than Small Entity	
Basic Fee		\$380.00 \$760.00	= \$ 760.00
Total Claims <u> </u> - <u>9</u>	—	X \$ 9.00 \$18.00	- \$ 00.00
Independent Claims <u> </u> - <u>3</u>	—	X \$ 39.00 \$78.00	\$ 00.00
First Presentation of Multiple Dependent Claim(s)	(0)	\$130.00 \$260.00	\$ 00.00
		Total	\$ 760.00

- If the difference is less than zero, enter "0".

Please charge Deposit Account No. 19-1457 in the amount of \$_____
A duplicate copy of this authorization is enclosed.

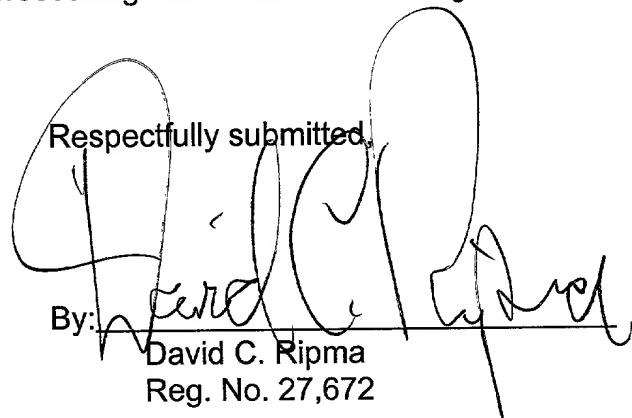
A check in the amount of \$ 800.00 to cover the filing fee (\$760.00)
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Any additional filing fees under 37 C.F.R. § 1.16.

Any patent application processing fees under 37 C.F.R. § 1.17.

Date: 3/17/99

Respectfully submitted,
By: 
David C. Ripma
Reg. No. 27,672

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METHOD FOR MODIFICATION OF POLISHING PATTERN DEPENDENCE IN CHEMICAL MECHANICAL POLISHING

Inventor: David Russell Evans

5

Field of the Invention

This invention relates to integrated circuit fabrication, and specifically to a method of shallow trench isolation.

Background of the Invention

10 Pattern dependence, i.e., variation of chemical mechanical polishing (CMP) rate due to feature size and density, is a severe problem for fabrication of shallow trench isolation (STI) for advanced integrated circuits. This results in the removal of too little or too much of the intended structure, and may also lead to contamination of the wafer. One
15 known way to overcome this problem is to include "dummy" structures in an IC for the sole purpose of controlling the CMP rate. This solution, however, places unacceptable restrictions on the design and functionality of IC devices.

20 Summary of the Invention

A method of CMP includes forming a CMP slurry containing cerium oxide; adding a slurry modifier to the slurry, wherein the slurry modifier polishes low structure areas at a substantially zero rate and polishes high structure areas at a rate approximating a blanket polishing rate; and polishing a structure using the modifier-contained slurry.

An object of the invention is to reduce or eliminate the effects of pattern dependence so that polishing rates will be substantially independent of feature size and density.

Brief Description of the Drawings

Fig. 1 depicts ideal polishing characteristics.

Fig. 2 depicts the characteristics of CMP of a silicon dioxide substrate using a conventional slurry.

5 Fig. 3 depicts CMP characteristic as a function of feature size and density.

Fig. 4 depicts the characteristics of CMP of a silicon dioxide substrate using a cerium oxide slurry.

10 Fig. 5 depicts the characteristics of CMP of a silicon dioxide substrate using a cerium oxide slurry with various pad sizes.

Fig. 6 depicts the characteristics of CMP of a silicon dioxide substrate using a cerium oxide slurry over a polish stop.

Fig. 7 depicts the effect of an ethylene glycol modifier on CMP using a cerium oxide slurry.

15 Fig. 8 depicts the effect of an ethylene glycol modifier on CMP using a cerium oxide slurry with increase down force.

Detailed Description of the Preferred Embodiment

The essence of the present invention is the use of a modifier to control polishing characteristics so that they approximate the behavior 20 shown in Fig. 1. The specific preferred implementation of this invention is for fabrication of shallow trench isolation (STI) for deep submicron integrated circuits.

Blanket removal rates are measured on unpatterned wafers.

Ideal polishing characteristics are depicted in Fig. 1, generally at 10.

25 Initially high structure areas, H, polish at a high polish rate 12 much faster than those low structure areas, L, which polish at a low area polish rate 14. In an ideal world, the low areas would not polish at all until the

high areas had been leveled to the height of the low areas. As topography is removed, these rates converge at a convergence point 16, after which, the wafer is polished at a converged rate 18, in a planarized area P, which is substantially the same as a blanket rate 20, until the polishing process 5 is terminated at 21. This convergence point, or planarization point, is dependent on feature size and density.

A typical real world polishing characteristic is shown in Fig. 2, generally at 22. A high structure area polish rate 24 is higher, meaning that high structure areas polish faster than any low structure 10 areas, which polish at a low area rate 26, however, the low structure areas are still polished and worn away long before the convergence point 28 is reached. Once the convergence point is reached, the converged rate of polishing 30 is similar to that of the idealized blanket rate 20.

Specific examples of polishing characteristics are depicted in 15 Fig. 3 for polishing data taken for four overall feature size scales. These are 2x2 μ m, Fig. 3a; 4x4 μ m, Fig. 3b; 8x8 μ m, Fig. 3c; and 16x16 μ m, Fig. 3d. For each size scale, drawn feature densities varying from 10% to 40% were measured, and a representative feature density of 25% is illustrated 20 in the drawing figures. Clearly, the real data follows the typical behavior illustrated in Fig. 2. It is also clear that both feature size and density affect the time required to planarize the surface. This data illustrates an inherent limitation of the conventional case, wherein the slurry is a strong alkali solution, such as sodium hydroxide, potassium hydroxide or ammonium hydroxide, which are mixed with fused silica.

25 In the course of investigation of the polishing behavior of silicon dioxide using a slurry formulation containing ceria, *i.e.*, cerium oxide, a different polishing characteristic is observed. This characteristic

is illustrated generally at 40 in Fig 4 and generally at 52 in Fig. 5.

Referring now to Fig. 4, a high polish rate 42 is greater than
blanket rate 20, which, for the purpose of comparison, are measured on
unpatterned wafers. A low area rate 44 converges with high area rate at
5 a convergence point 46, after which structure is removed at a converged
rate 48, until a polish stop rate 50 is encountered. An interesting feature
using this type of slurry in CMP is the appearance of an initial removal
rate 42a, for high areas of the features that is actually lower than blanket
removal rate 20. A pre-convergence high area rate 42b may be the same
10 as, or slightly greater than blanket removal rate 20. An initial low area
rate 44a and a pre-convergence low area rate are essentially zero.
Similarly, the removal rate for low features is essentially zero 46a.
Conventional models of chemical mechanical polishing cannot explain
such behavior.

15 An extreme case of this behavior is illustrated by
characteristic 52 depicted in Fig. 5. Again, polishing data taken for four
overall feature size scales. These are 2x2 μ m, Fig. 5a; 4x4 μ m, Fig. 5b;
8x8 μ m, Fig. 5c; and 16x16 μ m, Fig. 5d. For each size scale, drawn feature
densities varying from 10% to 40% were measured, and a representative
20 feature density of 25% is illustrated in the drawing figures. High area
rates 54 may or may not show initial and pre-convergence rates, while
low area rates 56 are nearly zero. Only in Fig. 5d, is a convergence point
58 reached, and then only in limited instances, *i.e.*, where the overall
feature density is less than 15%. Very little of the low areas are removed,
25 which is a desired characteristic, however, an insubstantial portion of the
high areas are removed, which does not achieve the desired planarization.
Clearly, It is only in the case of the largest scale features that substantial

planarization is achieved

If polishing using ceria slurry is carried out for a sufficiently long time, two important results occur: First, when the substrate surface becomes substantially planarized, polishing characteristics revert back to more conventional behavior. Second, if silicon, polysilicon, or silicon nitride underlie the polished layer of silicon dioxide, they tend to act as a polish stop. These results are illustrated in Figs 6a-6d, which depict polishing characteristics for feature size scales corresponding to Figs. 3 and 5. Long term ceria polishing characteristics are illustrated generally at 60. A high area rate 62 approximates blanket rate 20 over time, while a low area rate 64 is close to zero. Convergence points 66 are reached in some instances, with a converged rate 68 being shown for those instances. Where an appropriate underlying material is present, a polish stop rate 70 is shown.

Further testing has demonstrated that addition of a modifier to ceria slurry, in particular ethylene glycol, causes the polishing characteristics illustrated in Figs. 4, 5 and 6 to become more conventional, as illustrated in Figs. 2 and 3. This characteristic is shown generally at 80 in Fig. 7, which illustrates the effect of addition of 10% (Fig. 7a) and 20% (Fig. 7b) ethylene glycol, and a down pressure of 6 psi.. A high area rate 82 is approximately the same as blanket rate 20, while a low area rate 84 remains close to zero. Characteristics 82a, 84a, represent a feature size scale of $2 \times 2 \mu\text{m}$; characteristics 82b, 84b, represent a feature size scale of $4 \times 4 \mu\text{m}$; characteristics 82c, 84c, represent a feature size scale of $8 \times 8 \mu\text{m}$; and characteristics 82d, 84d, represent a feature size scale of $16 \times 16 \mu\text{m}$. A convergence point 86 is depicted, but not really achieved in the depicted results. Longer polishing times result in the desired

convergence and planarization.

A 10%, by volume, addition of ethylene glycol results in the polishing rate of high areas and the blanket polishing rate to be nearly equal. In comparison, for addition of 20% ethylene glycol by volume, the removal rate has fallen below the blanket polish rate. This is similar to conventional behavior. As expected, control wafers polished in slurry with no ethylene

glycol modifier show the usual behavior. Ethylene glycol concentrations up to 50% may be used.

The effect of the modifier can be offset by increasing down force. The effect of a 50% increase in down force, to 9 psi, for a 20% by volume addition of ethylene glycol to ceria slurry is shown in Fig. 8, at 90. A high area rate 92, low area rate 94 and convergence point 96 are depicted, with the characteristics being as described in connection with Fig. 7. The preferred modifier is ethylene glycol, however, other modifiers may work as well provided that they alter physical properties of the slurry in the desired way. The modifier may either be pre-mixed in the slurry feedstock or introduced directly to the polishing table to be mixed with slurry during processing. Ideally, the modifier does not participate in the slurry chemistry, but, only affects slurry physical characteristics, i.e., viscosity, surface tension, etc. or concomitantly, effective slurry layer thickness during the polishing process.

The ideal polishing characteristic of Fig. 1 implies that all pattern dependence may be eliminated because, regardless of size or density, high structure areas polish at the same rate as a flat blanket wafer. Therefore, as the invention is implemented in conjunction with a judicious choice of polishing parameters such as down force, *etc.*, the

polishing characteristics of Fig. 1 may be closely approximated. When practicing the preferred embodiment of the invention, a down force of five to ten psi is applied. A table rotation rate of about 20 to 100 rpm is established, as is a spindle rotation rate of about 20 to 100 rpm. A slurry flow rate of 0.50 to 500 ml/min. is maintained.

In a more universal sense, the method of the invention may be used for general global planarization. Use of this method will increase process margin and reduce or eliminate the need to include dummy structures in circuit layouts.

Although a preferred embodiment of the method of the invention has been disclosed herein, it will be appreciated that further variations and modification may be made thereto without departing from the scope of the invention as defined in the appended claims.

I claim:

1. A method of CMP comprising:
 - forming a CMP slurry containing cerium oxide;
 - adding a slurry modifier to the slurry, wherein the slurry modifier polishes low structure areas at a substantially zero rate and polishes high structure areas at a rate approximating a blanket polishing rate; and
 - polishing a structure using the modifier-contained slurry.
2. The method of claim 1 wherein said forming includes setting a cerium oxide concentration of between about 1% and 50% by weight.
3. The method of claim 1 wherein said polishing includes CMP at a pressure of between about five psi and ten psi.
4. The method of claim 1 wherein said adding includes adding ethylene glycol at a concentration of up to 50%.
5. A method of CMP comprising:
 - forming a CMP slurry containing cerium oxide at a concentration of between about 1% and 50% by weight;
 - adding a slurry modifier to the slurry, wherein the slurry modifier polishes low structure areas at a substantially zero rate and polishes high structure areas at a rate approximating a blanket polishing rate; and
 - polishing a structure using the modifier-contained slurry.

6. The method of claim 5 wherein said polishing includes CMP at a pressure of between about five psi and ten psi.

7. The method of claim 5 wherein said adding includes adding ethylene glycol at a concentration of up to 50%.

8. A method of CMP comprising:

forming a CMP slurry containing cerium oxide at a

concentration of between about 1% and 50% by weight;

adding ethylene glycol at a concentration of up to 50% for

5 polishing low structure areas at a substantially zero rate and polishing high structure areas at a rate approximating a blanket polishing rate; and polishing a structure using the slurry.

9. The method of claim 8 wherein said polishing includes CMP at a pressure of between about five psi and ten psi.

Abstract of the Disclosure

A method of CMP includes forming a CMP slurry containing cerium oxide; adding a slurry modifier to the slurry, wherein the slurry modifier polishes low structure areas at a substantially zero rate and 5 polishes high structure areas at a rate approximating a blanket polishing rate; and polishing a structure using the modifier-contained slurry.

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PENDENCE IN CHEMICAL)	
MECHANICAL POLISHING)	

POWER OF ATTORNEY BY ASSIGNEE

Sharp Microelectronics Technology, Inc., a corporation of the State of Washington, Assignee of the entire right, title and interest in and to the above-identified patent application by an assignment document filed herewith, hereby appoints David C. Ripma, Reg. No. 27,672, and Gerald Maliszewski, Reg. No. 38,054, as its attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith; said appointment to be to the exclusion of the inventor and the inventor's attorneys in accordance with the provisions of 37 C.F.R. §3.71.

Pursuant to 37 C.F.R. §3.73(b) the undersigned submits herewith the original of an assignment document from the inventor or inventors to Assignee, together with a cover sheet and the fee for recording same. The assignment has been reviewed and, to the best of the undersigned's knowledge

and belief, title is in the Assignee. The undersigned, whose title is supplied below, is empowered to sign this power of Attorney on behalf of the Assignee.

Please direct all correspondence connected with this application to:

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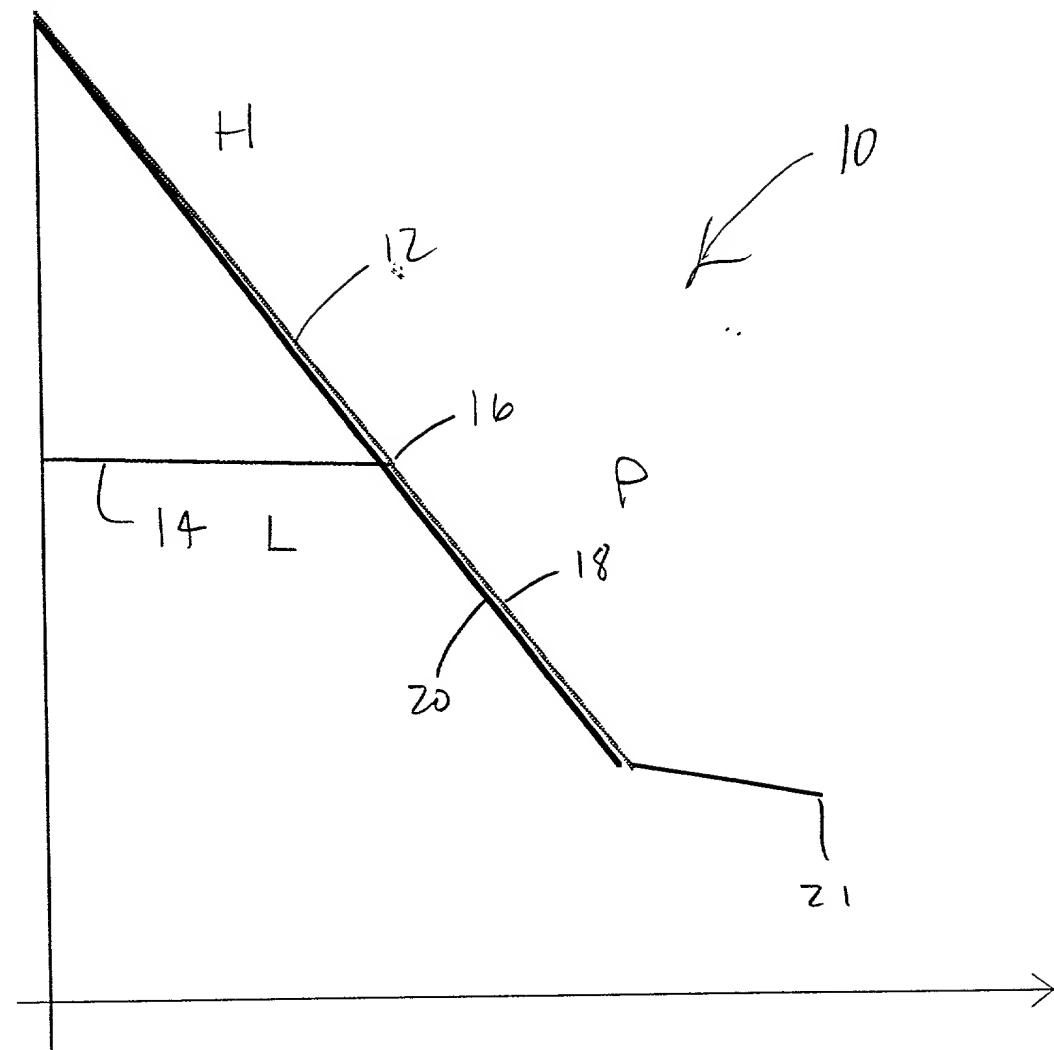
Assignee:

Sharp Microelectronics Technology, Inc.

By: Joseph Pinzone
Title: Vice President

Date: 3-17-99

Height



Polish Time

Fig. 1

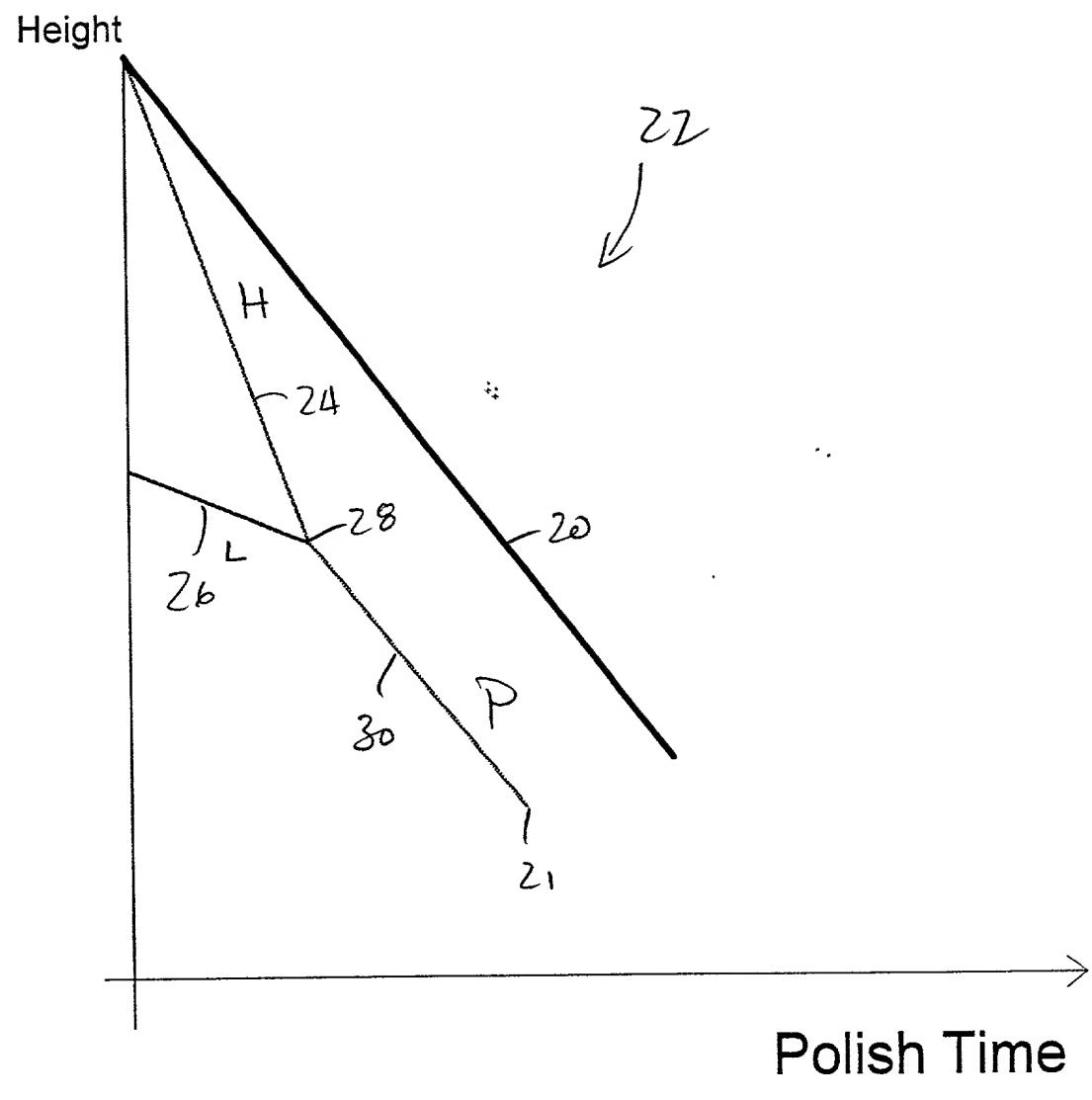


Fig. 2

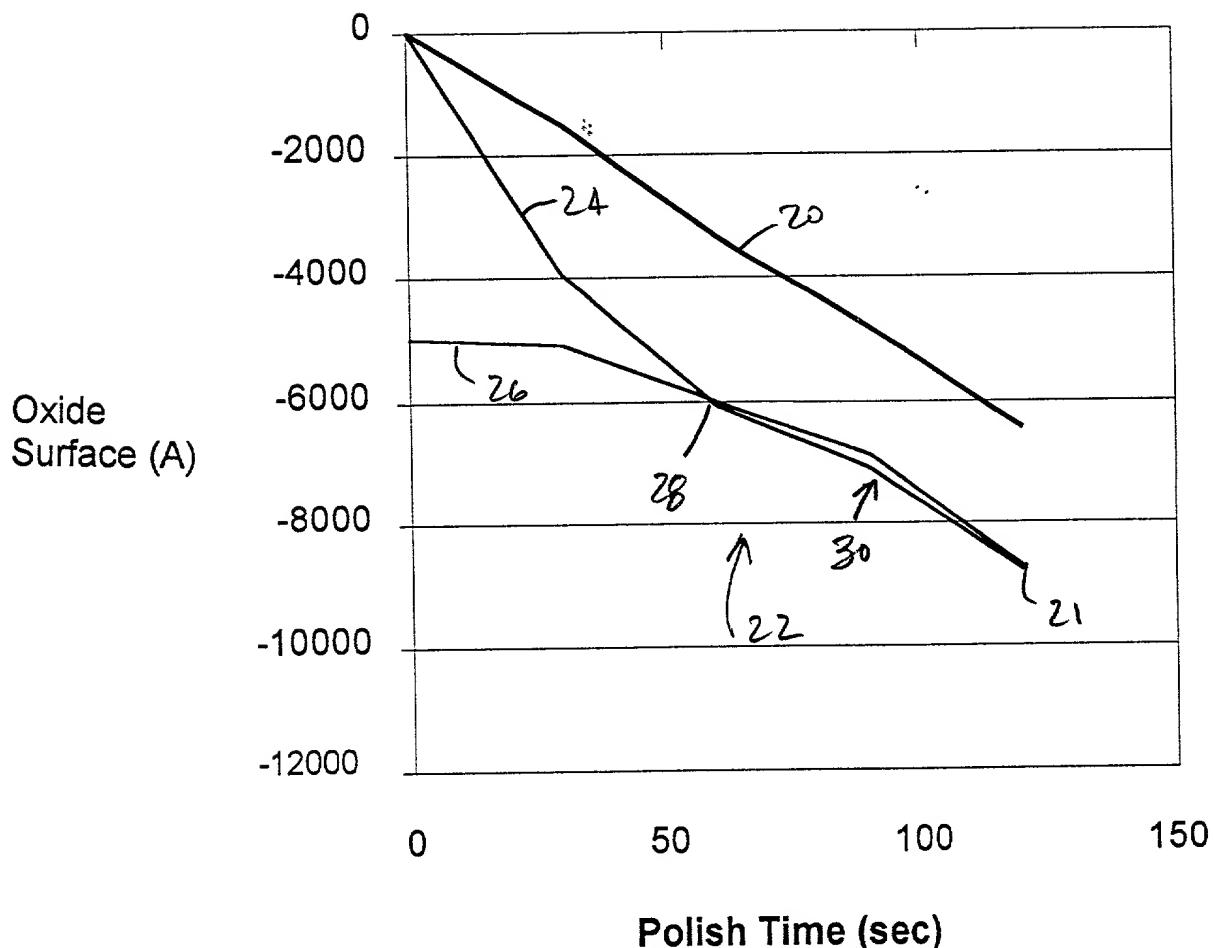


Fig. 3a

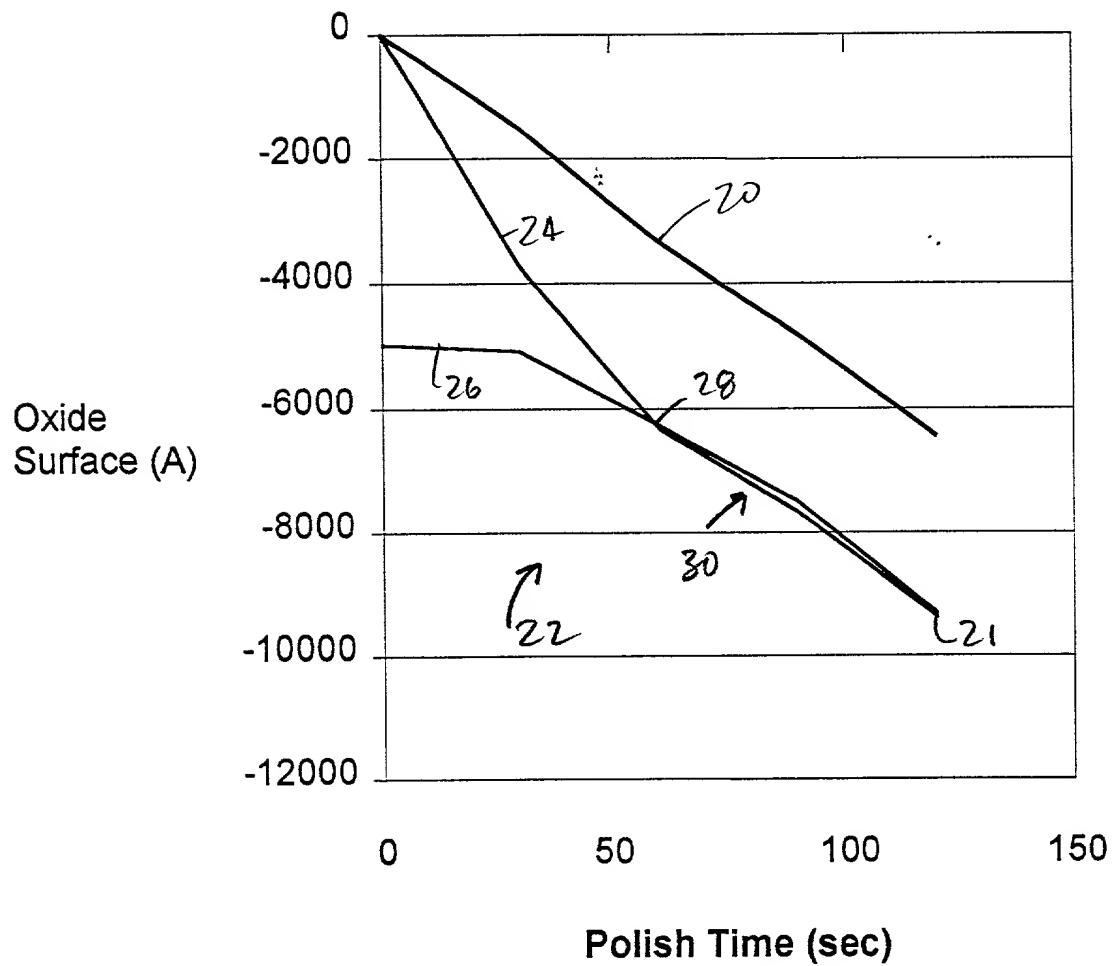


Fig. 3b

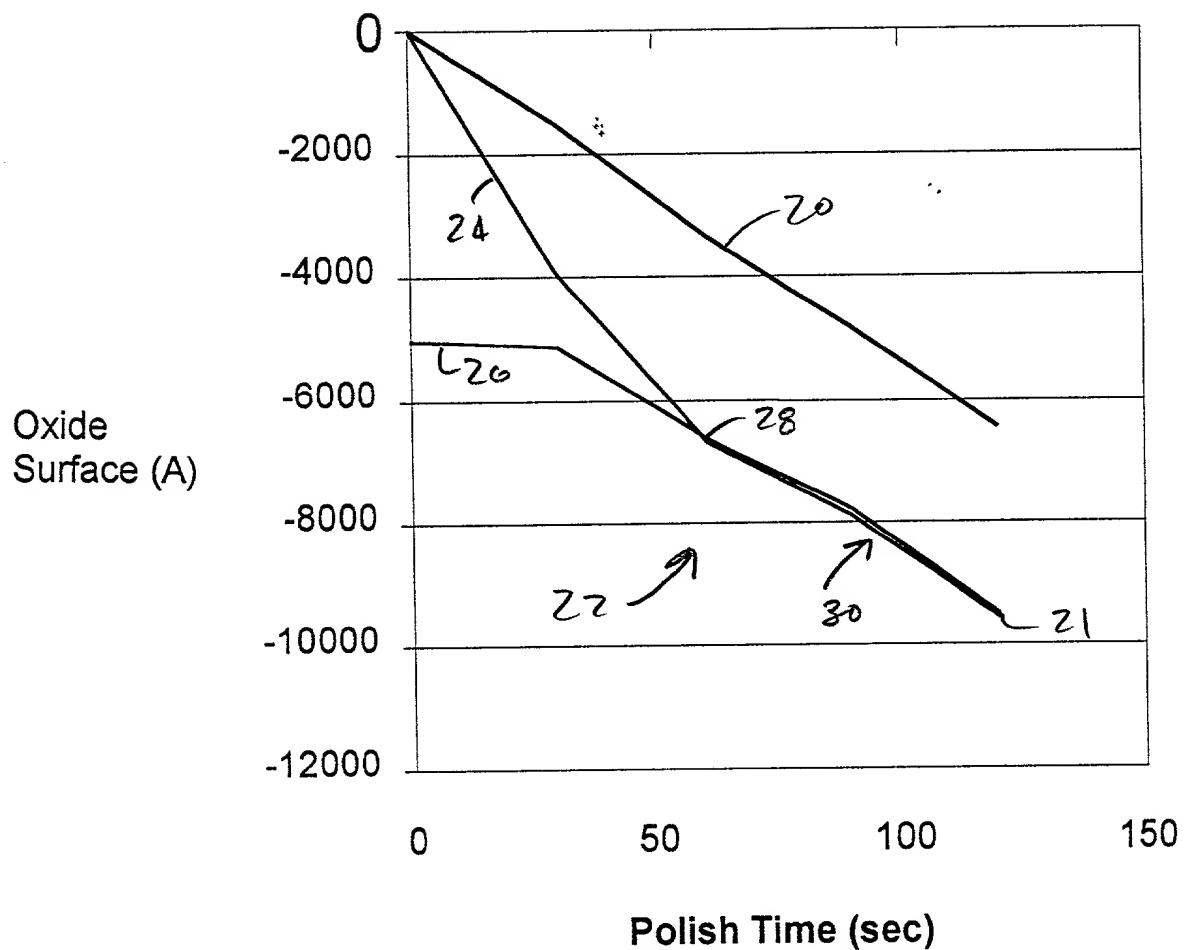


Fig. 3c

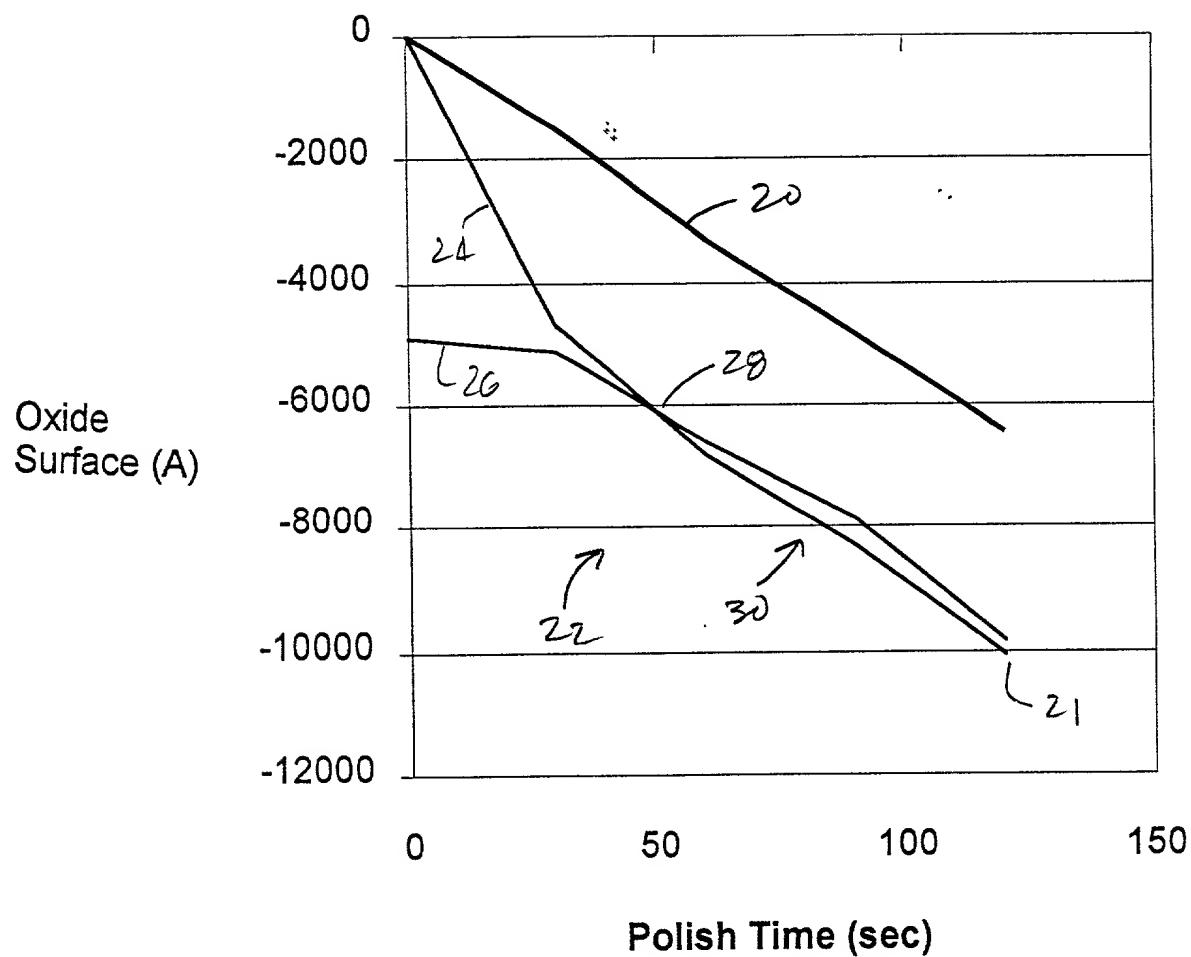
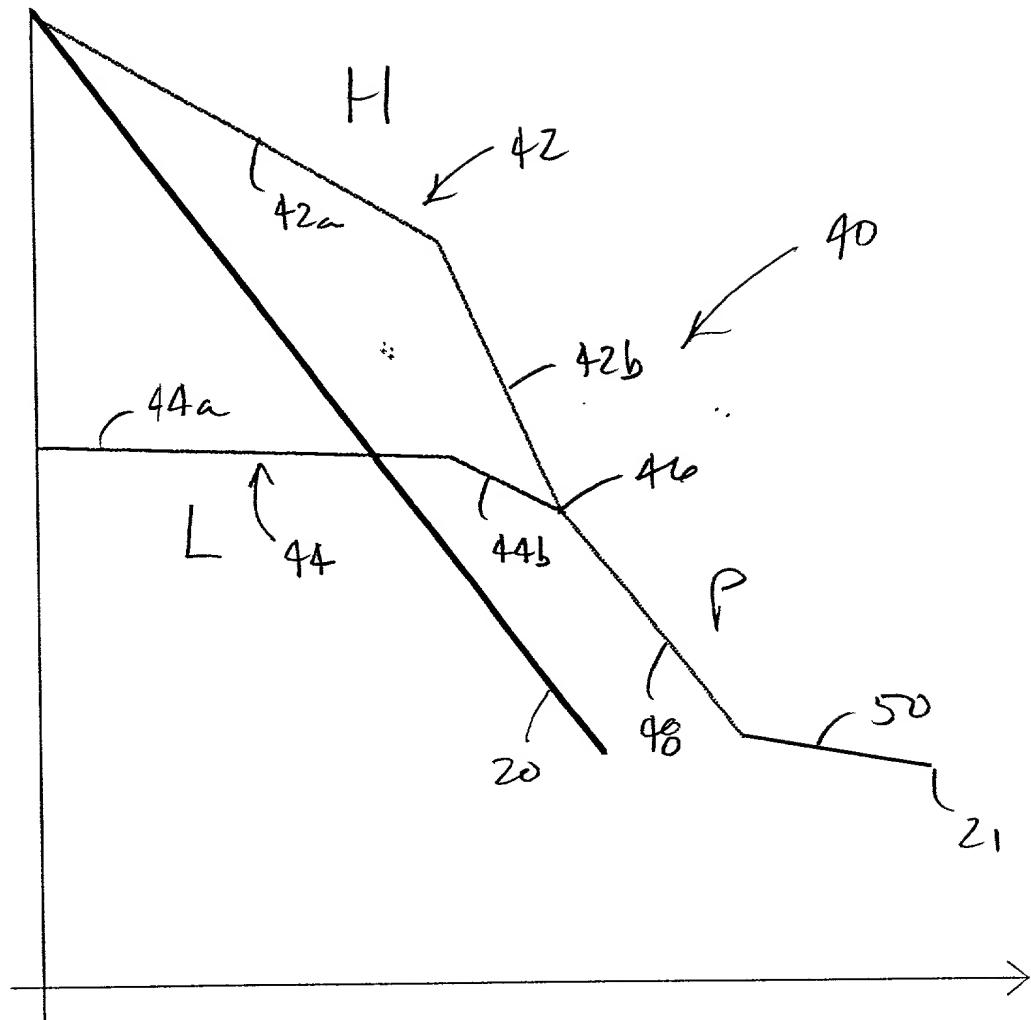


Fig. 3d

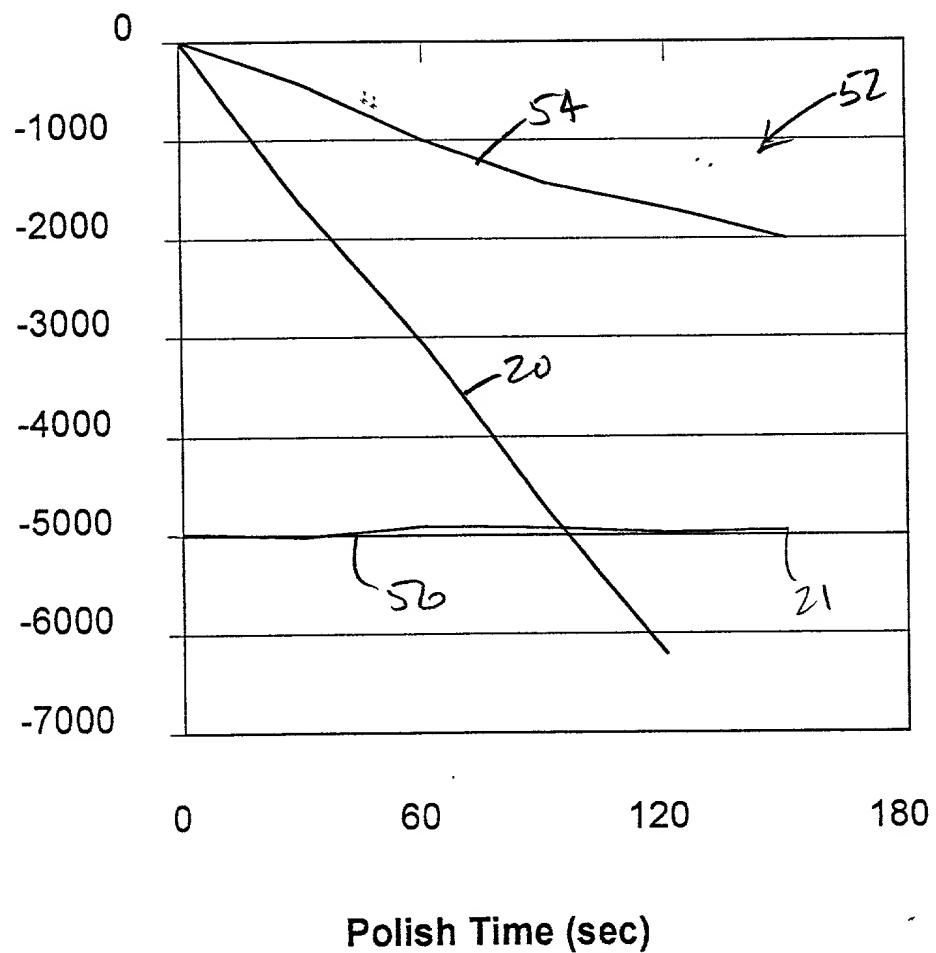
Height



Polish Time

Fig. 4

Oxide
Surface (A)



Polish Time (sec)

Fig. 5a

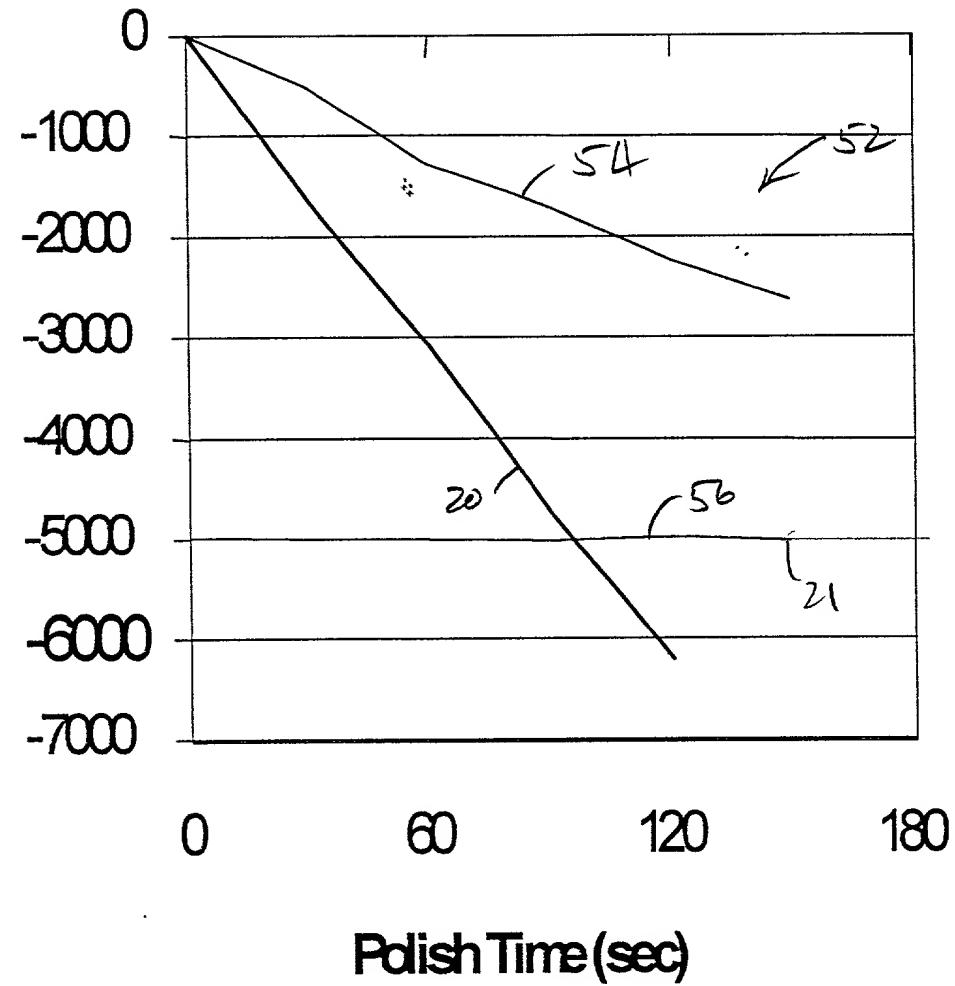


Fig. 5b

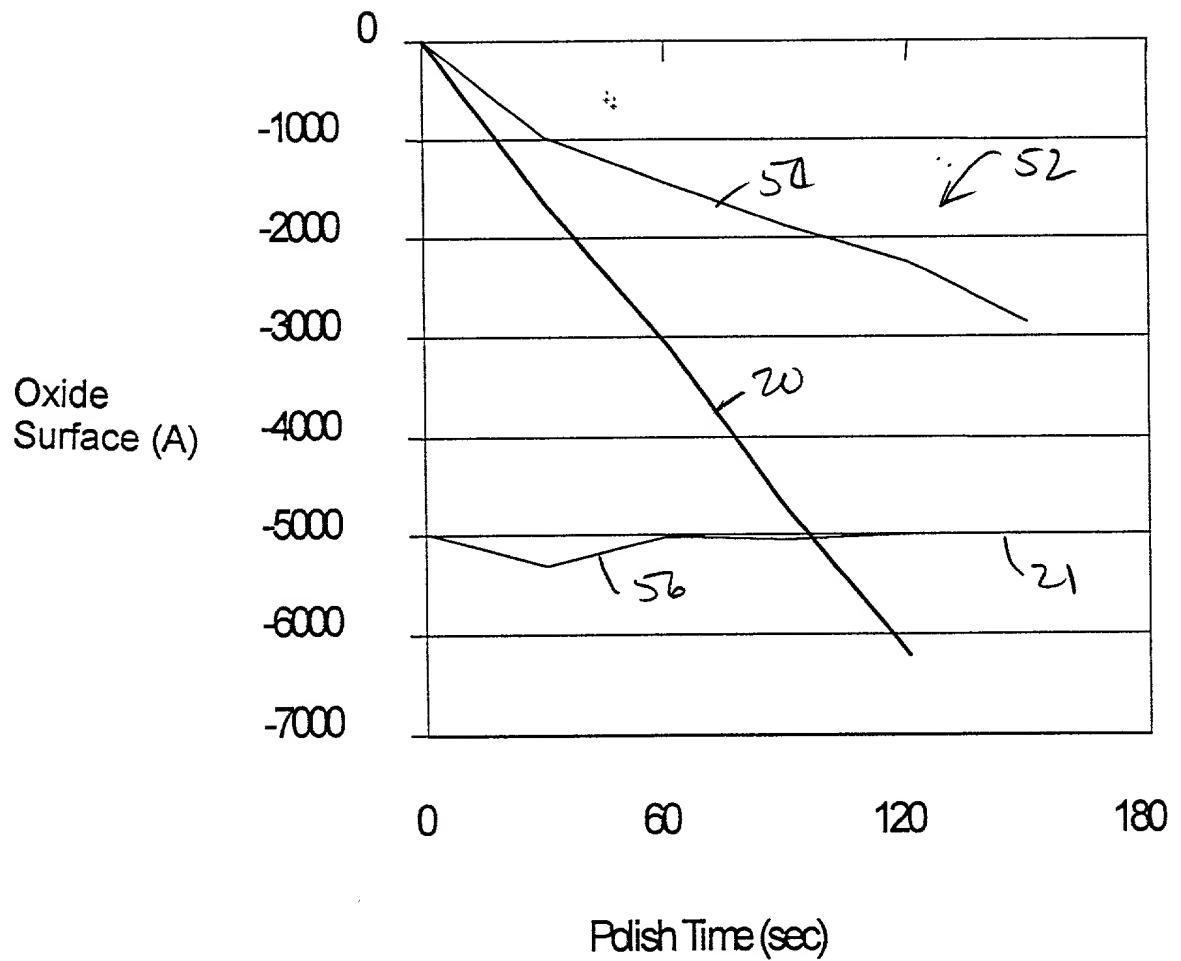


Fig. 5c

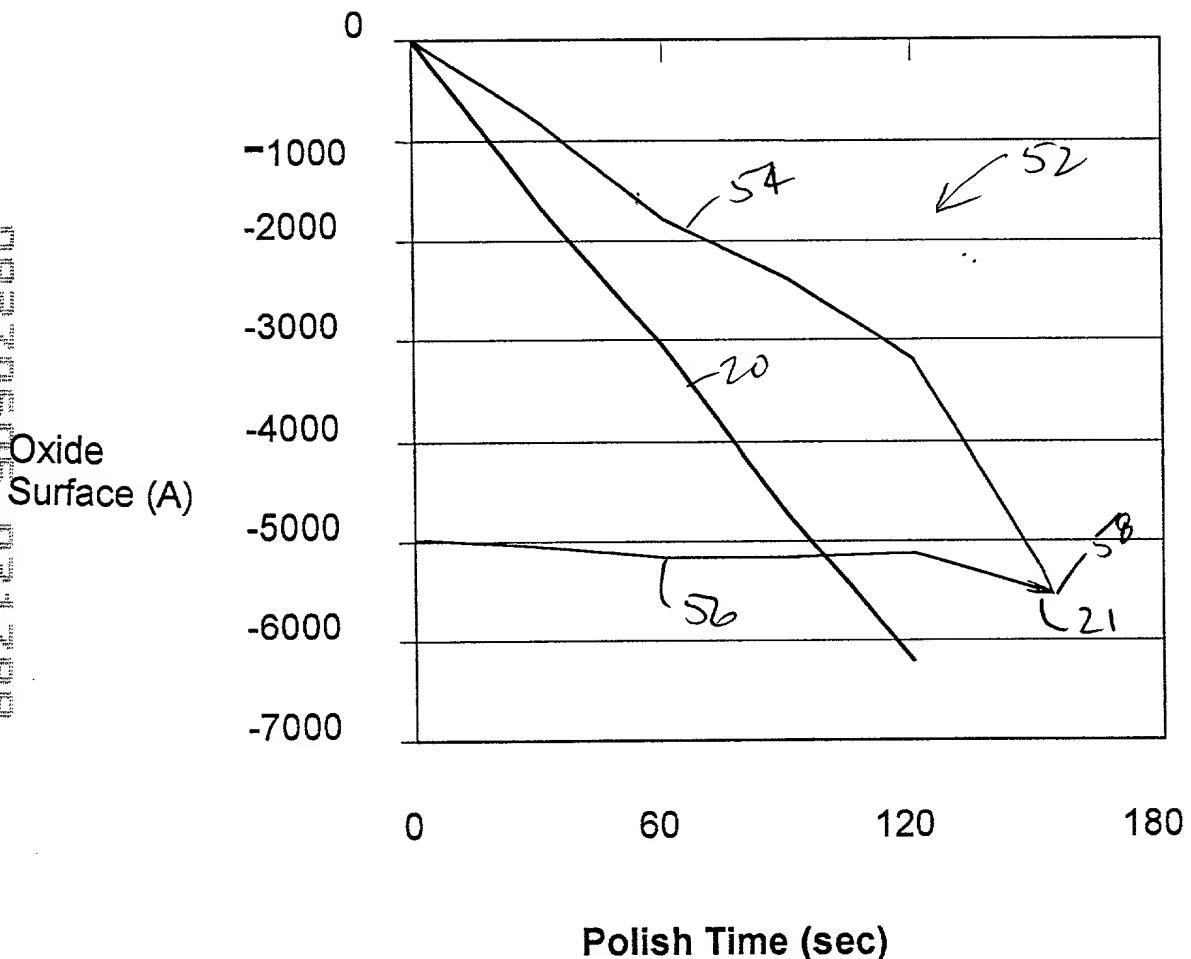


Fig. 5d

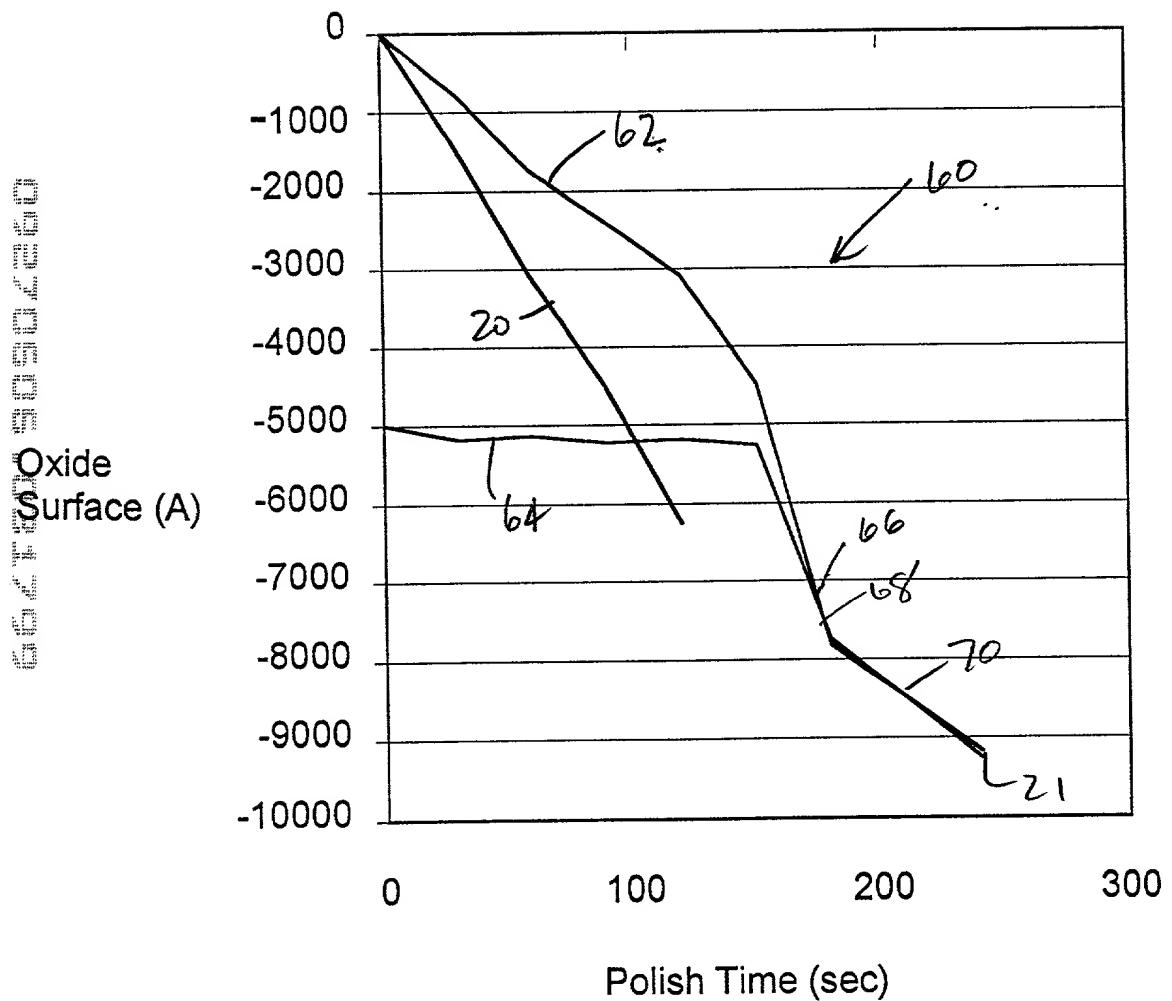


Fig. 6a

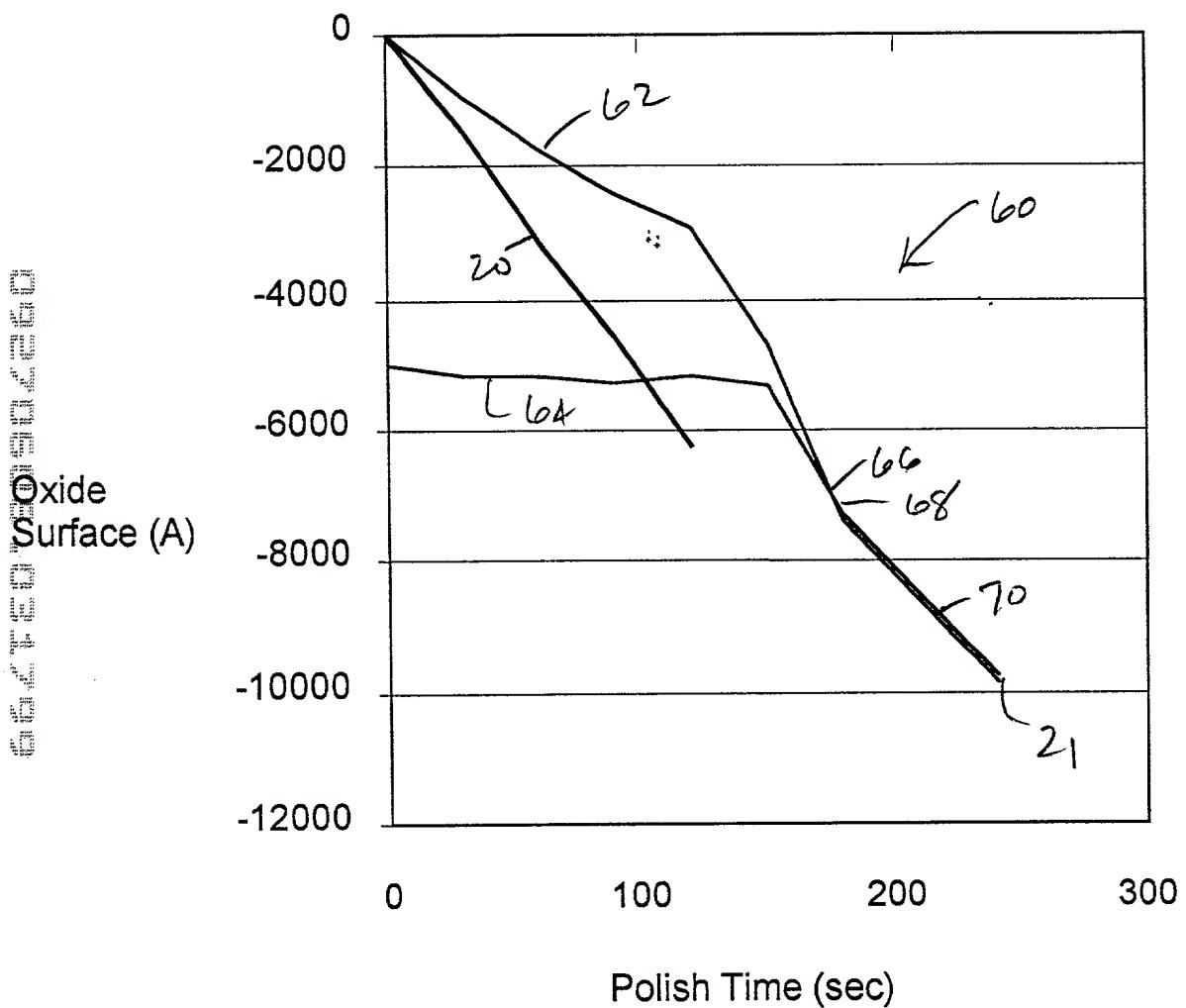


Fig. 6b

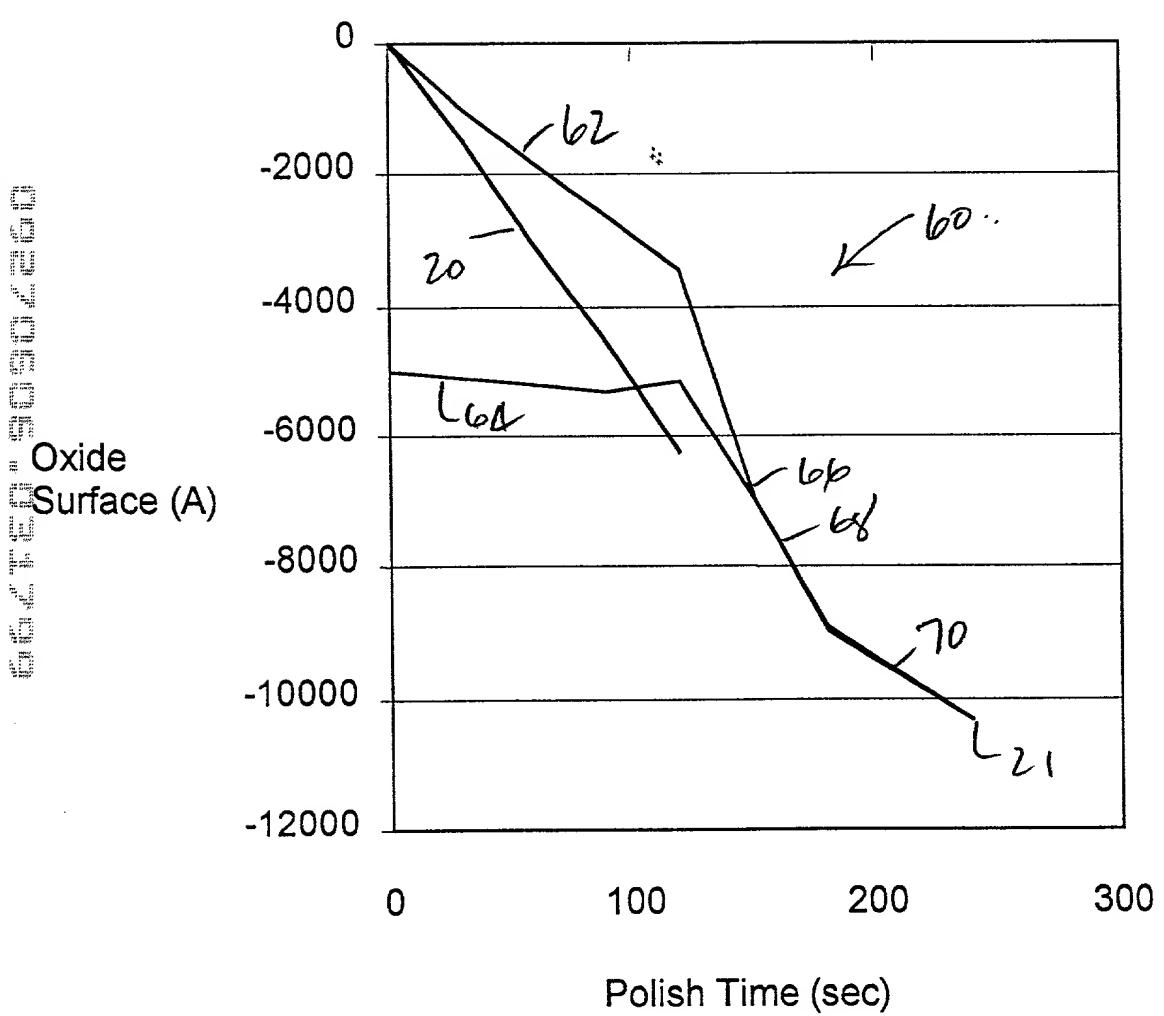


Fig. 6c

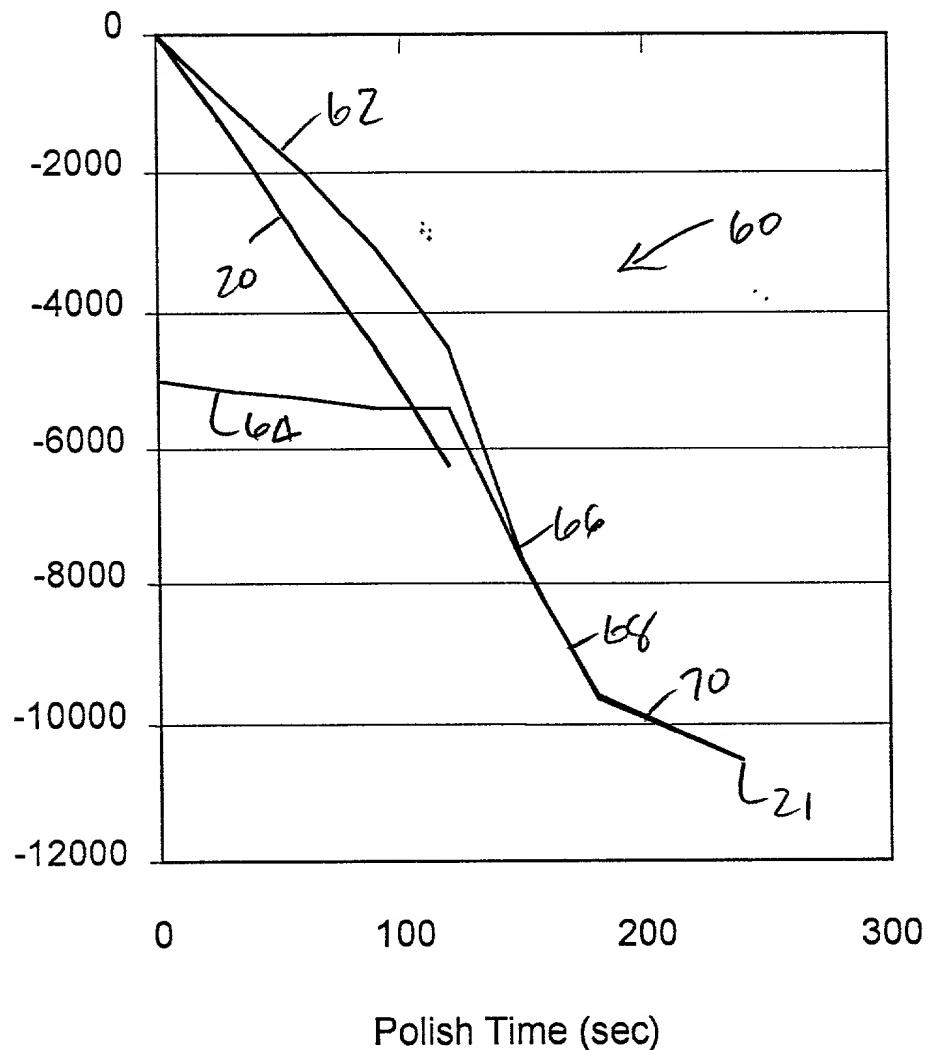


Fig. 6d

Oxide Surface (Å)

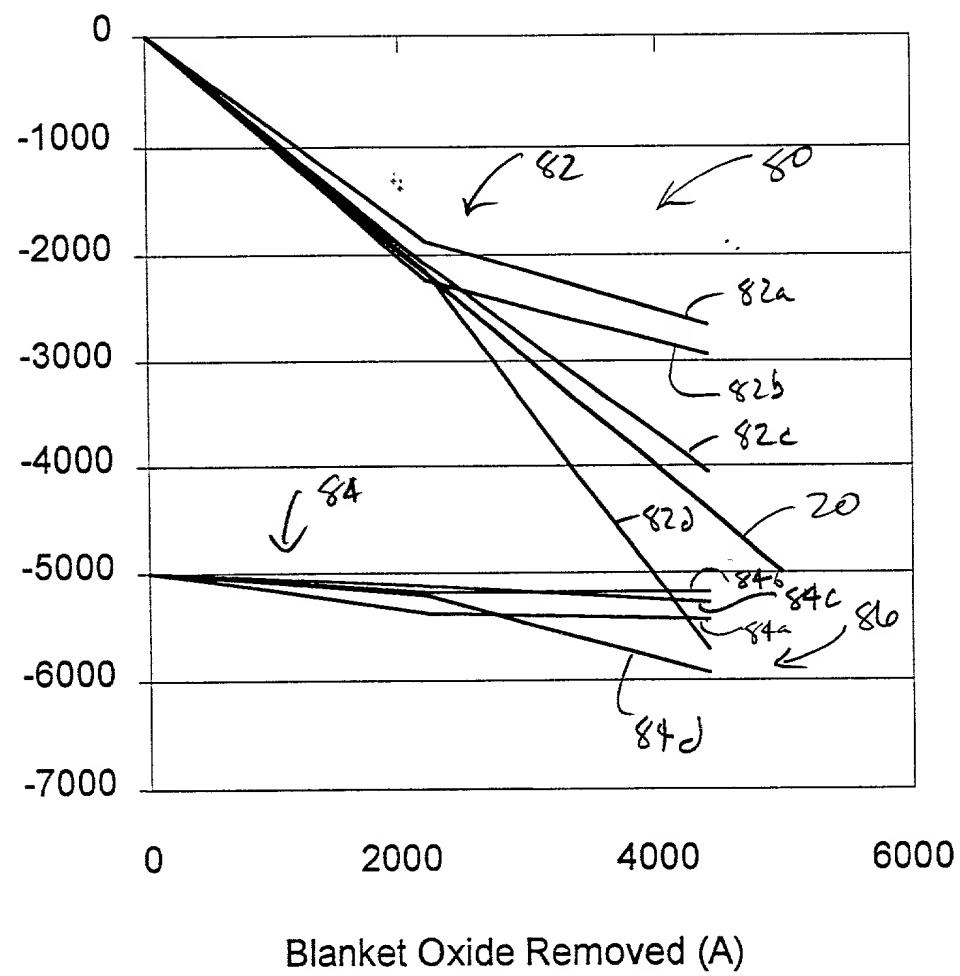


Fig. 7a

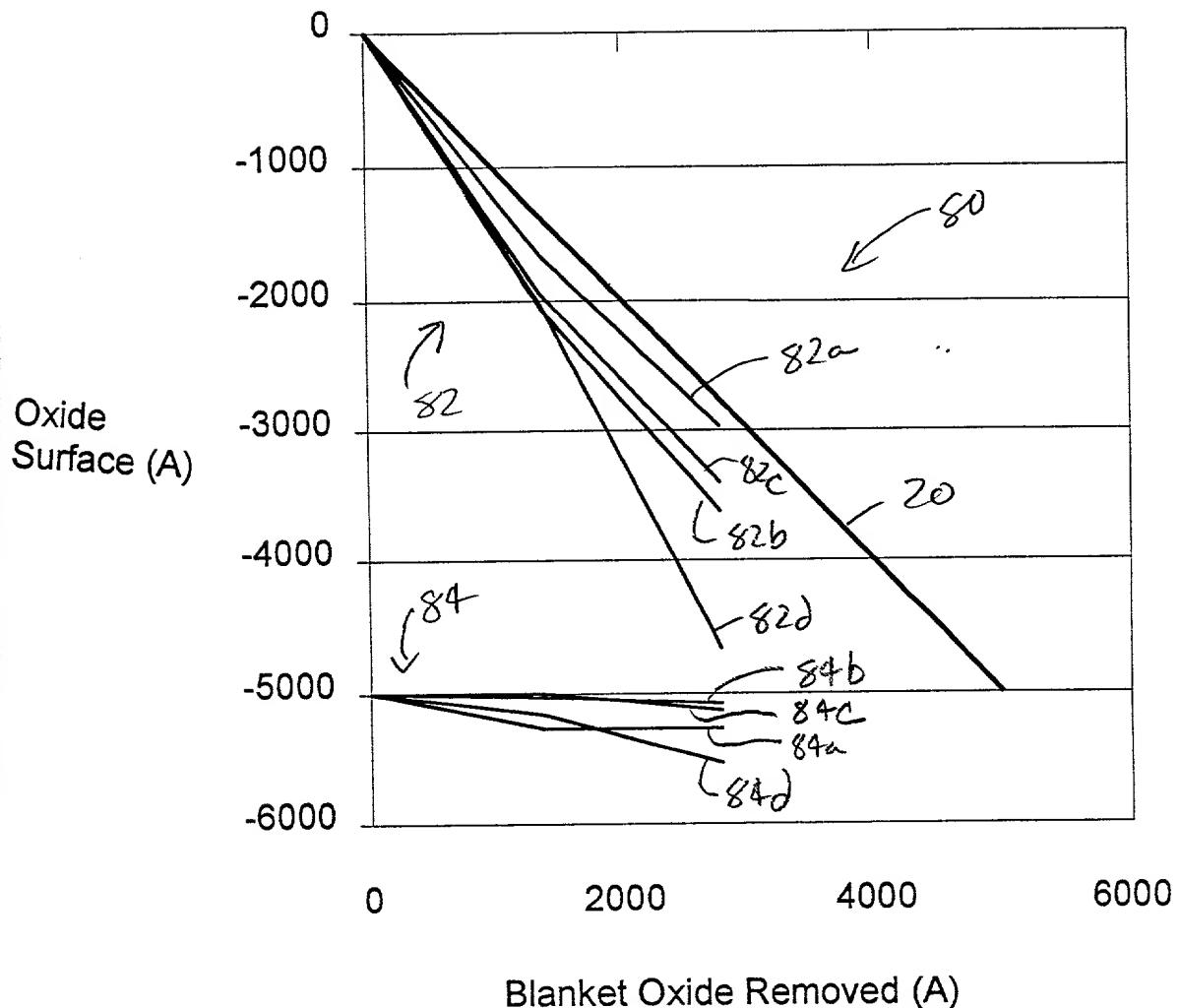


Fig. 7b

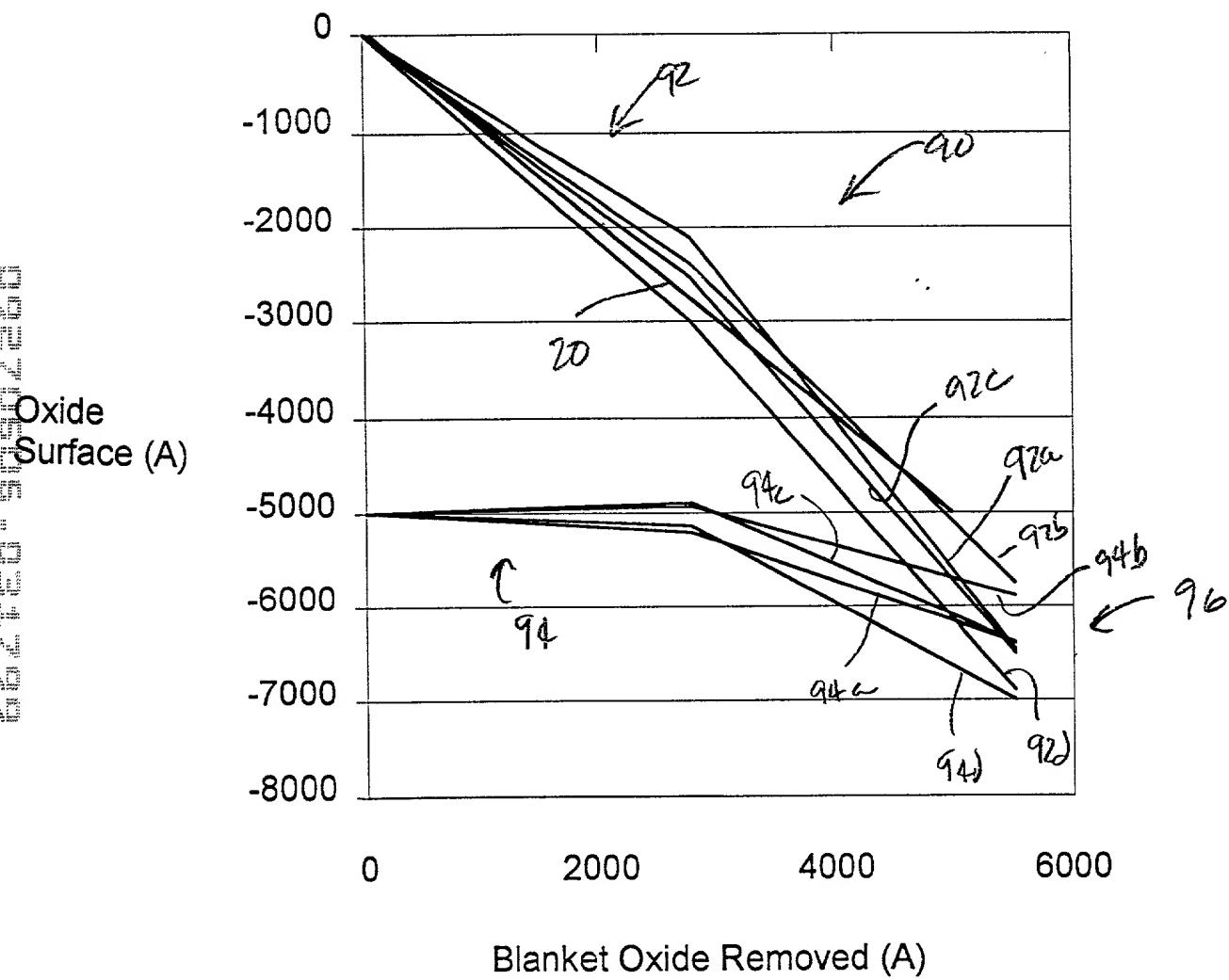


Fig. 8

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Title: METHOD FOR MODIFICATION OF POLISHING PATTERN DE- PENDENCE IN CHEMICAL MECHANICAL POLISHING)	
)	

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name; I believe that I am the original, first and sole inventor (if one name is listed below), or the first and joint inventor (if plural names are listed below), of the subject matter which is claimed and for which a patent is sought on the invention entitled,

**METHOD FOR MODIFICATION OF POLISHING PATTERN
DEPENDENCE IN CHEMICAL MECHANICAL POLISHING**

the specification of which (check applicable ones):

- is attached hereto;
- was filed with the above-identified "Filed" date and assigned the above-identified "Serial No.;"
- was amended on (or amended through) _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by an amendment(s) referred to above. I acknowledge the duty to disclose information which is material to the examination of the application in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full name of sole
or first inventor: David Russell Evans

Residence: 7574 SW 179th Place
Beaverton, OR 97007

Post Office Address: Same

Citizenship: United States

Inventor's signature: 

Date: 17/Mar/1999